



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 2214

Tsuyoshi TOMITA

Art Unit: 2631

Serial No.: 09/263,766

Examiner: K. M. Burd

Filed: March 5, 1999

Docket No.: 108075-09006

For: SIGNAL PROCESSING CIRCUIT FOR PREVENTING PSEUDO LOCK OF
TIMING RECOVERY PLL

AMENDMENT UNDER 37 C.F.R. § 1.116

Introductory Comments

Mail Stop AF

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

March 3, 2005

Sir:

In response to the Office Action dated October 12, 2004, the period for response being extended from January 12, 2005 until March 12, 2005, please amend the above-identified application as set forth below.

*please enter
after final and
Thank you
KMB*